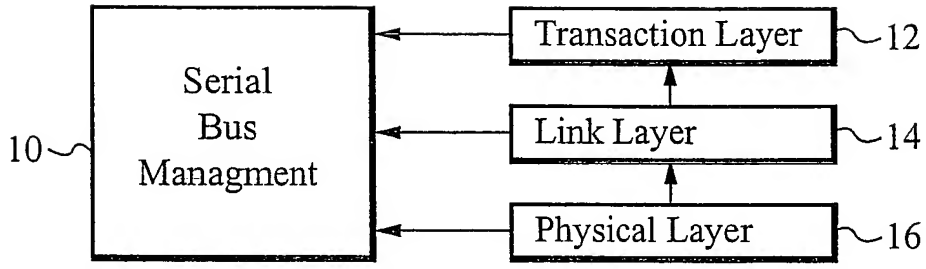

Fig. 1 (PRIOR ART)

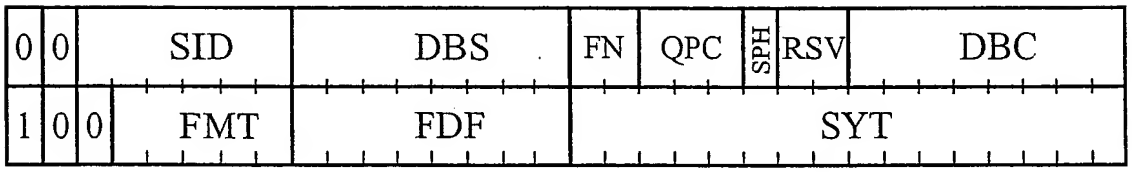

Fig. 2

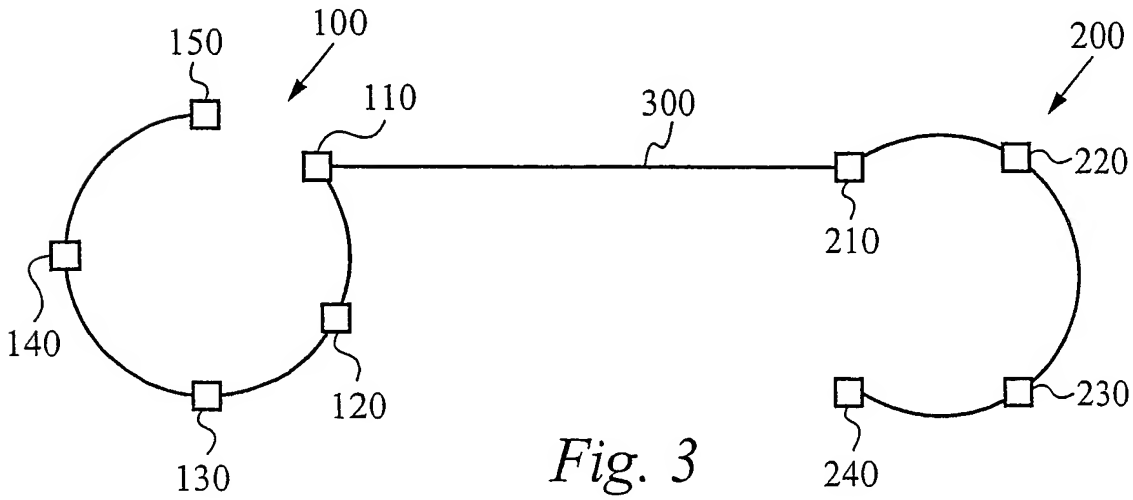

Fig. 3

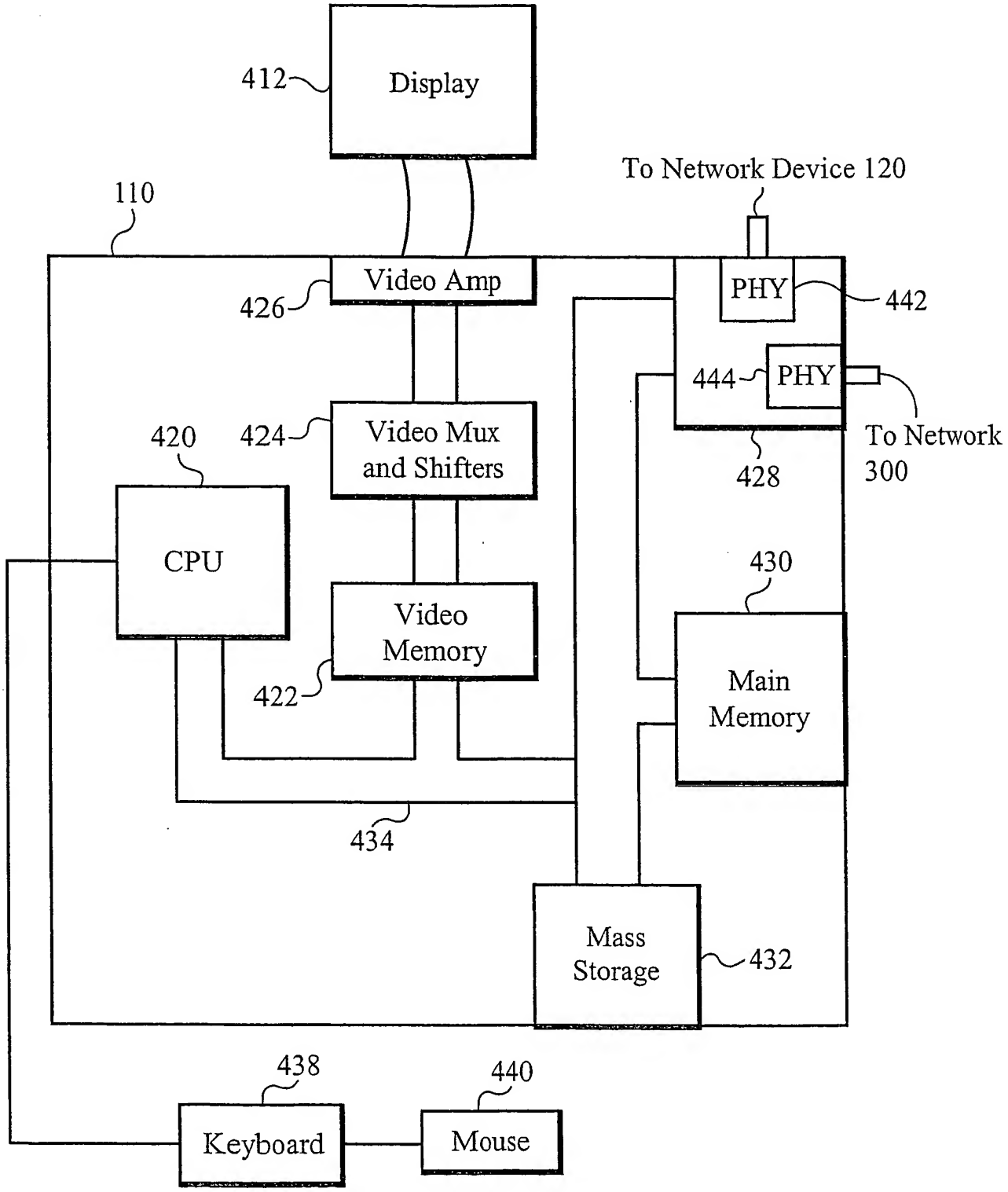
412
Display
To Network Device 120
110
Video Amp
426
PHY
442
444
PHY
To Network
300
428
420
424
Video Mux
and Shifters
CPU
430
Video
Memory
422
Main
Memory
434
Mass
Storage
432
438
440
Keyboard
Mouse

Fig. 4

CYCLE SEC
CYCLE COUNT
CYCLE OFFSET

Fig. 5

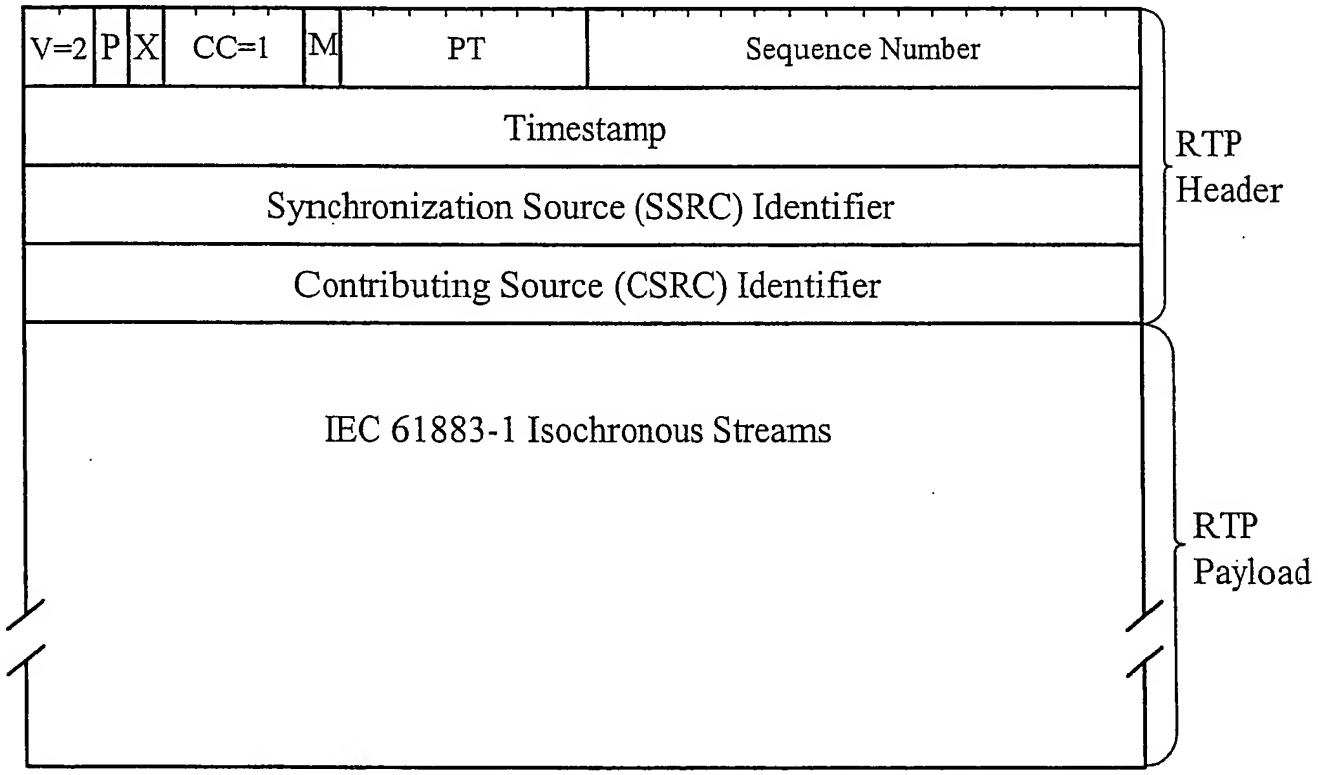
V=2
P
X
CC=1
M
PT
Sequence Number
Timestamp
Synchronization Source (SSRC) Identifier
Contributing Source (CSRC) Identifier
IEC 61883-1 Isochronous Streams
RTP Header
RTP Payload

Fig. 6

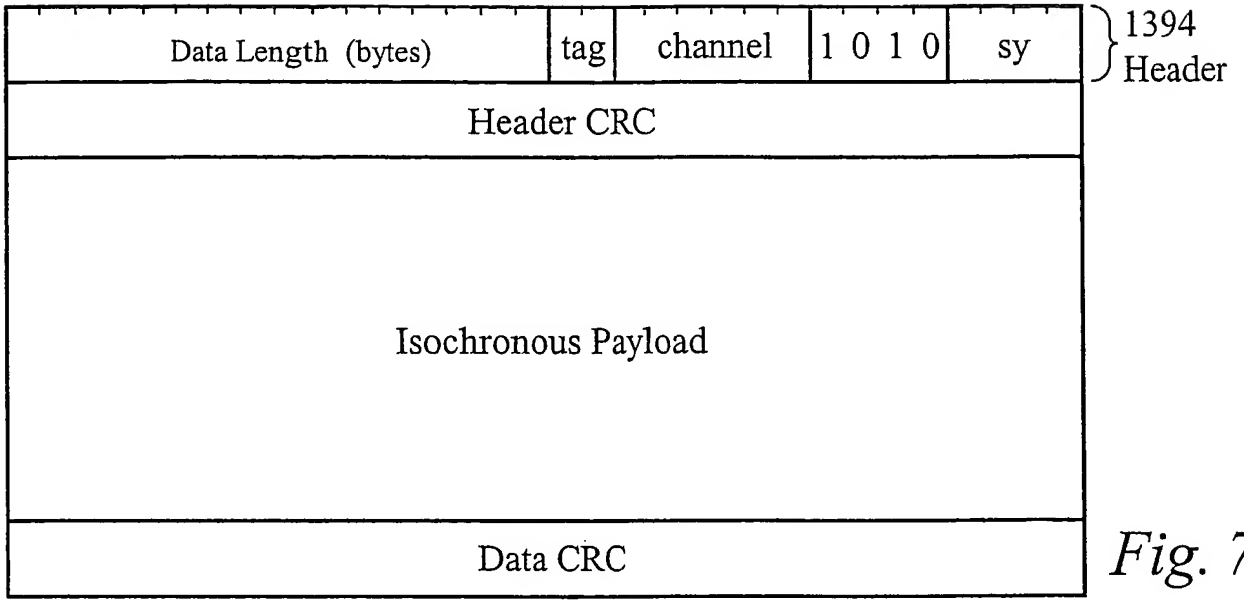
Data Length (bytes)
tag
channel
1 0 1 0
sy
1394 Header
Header CRC
Isochronous Payload
Data CRC

Fig. 7

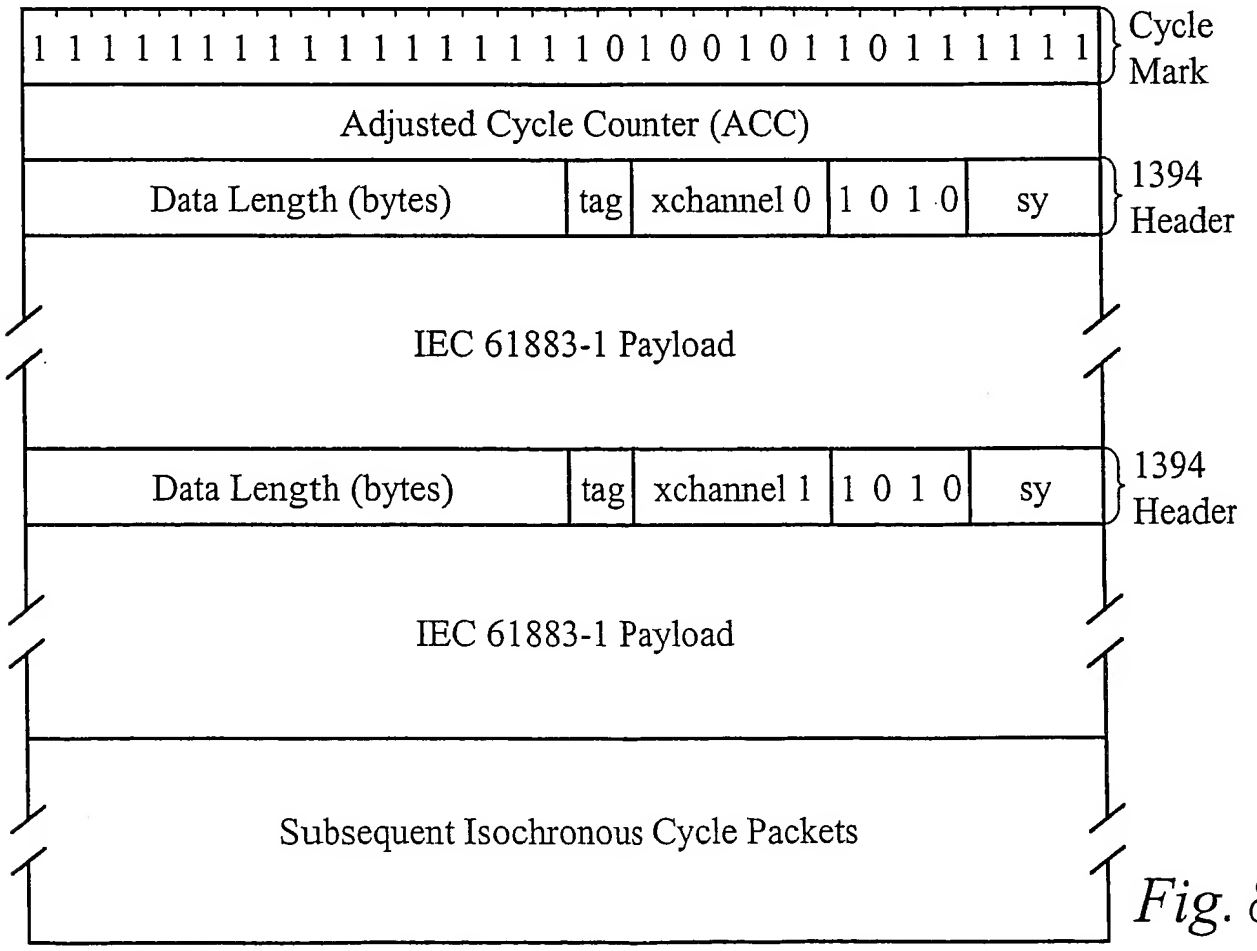
1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 0 1 0 0 1 0 1 1 0 1 1 1 1 1 1
Cycle Mark
Adjusted Cycle Counter (ACC)
Data Length (bytes)
tag
xchannel 0
1 0 1 0
sy
1394 Header
IEC 61883-1 Payload
Data Length (bytes)
tag
xchannel 1
1 0 1 0
sy
1394 Header
IEC 61883-1 Payload
Subsequent Isochronous Cycle Packets

Fig. 8